PTO/SB/08A (08-03)

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Substitute for form 1449/PTO

Sheet 1

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

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Application Number	10/766,786			
Filing Date	ng Date January 27, 2004			
First Named Inventor	Guterman			
Art Unit				
Examiner Name				
Attorney Docket Number	SAND-01015US0			

Complete if Known

			U. S. PATENT	DOCUMENTS	
Examiner Initials*	Cite No.1	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant
	<u> </u>	Number-Kind Code ² (F known)			Figures Appear
		^{US-} 5,220,531	06/15/1993	Blyth et al.	
		^{US-} 5,313,421	05/17/1994	Guterman et al.	
		^{US-} 5,412,601	05/02/1995	Sawada et al.	
		^{US-} 5,521,865	05/28/1996	Ohuchi et al.	
		^{US-} 5,570,315	10/29/1996	Tanaka et al.	
		^{US-} 5,652,719	07/29/1997	Tanaka et al.	
		^{US-} 5,712,180	01/27/1998	Guterman et al.	
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		^{US-} 5,761,222	06/02/1998	Baldi	
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		^{US-} 5,949,714	09/07/1999	Hemink et al.	
		^{US-} 6,151,248	11/21/2000	Harari et al.	
		^{US-} 6,222,762	04/24/2001	Guterman et al.	
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		^{US-} 6,301,161	10/09/2001	Holzmann et al.	
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		^{US-} 6,522,580	02/18/2003	Chen et al.	
	1	^{US-} 6,525,964	02/25/2003	Tanaka et al.	

	FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No.1	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages		
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Sheet 2

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		^{US-} 6,529,412	03/04/2003	Chen et al.	
		^{US-} 6,532,172	03/11/2003	Harari et al.	
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,		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	,	KURATA, Hideaki, et al., Constant-Charge-Injection Programming for 10-MB/s Multilevel AG-AND Flash Memories, 2002 Symposium On VLSI Circuits Digest of Technical Papers, pp. 302-303	
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		NOBUKATA, Hiromi, et al., A 144Mb 8-Level NAND Flash Memory with Optimized Pulse Width Programming, 1999 Symposium on VLSI Circuits Digest of Technical Papers, pp. 39-40	
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Signature	Considered	

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